

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application Serial No. 09/059,644  
Filing Date April 13, 1998  
Inventor Pai-Hung Pan  
Assignee Micron Technology, Inc.  
Group Art Unit 2822  
Examiner M. Trinh  
Attorney's Docket No. MI22-898  
Title: Semiconductor Processing Methods of Forming a Conductive Gate and Line

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

PURSUANT TO 37 C.F.R. §§ 1.56, 1.97 AND 1.98 FAX COPY RECEIVED  
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In compliance with 37 C.F.R. §§ 1.56, 1.97 and 1.98, the Examiner's attention is directed to the references listed on the attached Form PTO-1449 and copies of which are attached. No admission is made regarding whether all the submitted references are prior art.

Citation of these patents is respectfully requested.

Respectfully submitted,

Dated: May 9, 2000

By: Bernard Berman  
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1 Inventor: Pai-Hung Pan

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3 Gate and Line

4 Assignee: Micron Technology, Inc.

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17 Date: Apr 18, 2000

18 Attorney: Bernard Berman

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